TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

TA8231L

42W BTL×2CH AUDIO POWER IC

The TA8231L is BTL audio power amplifier for consumer application. It is designed for high power, low distortion and low noise.

It contains various kind of protectors and the function of stand-by.

FEATURE

- High Power
 - : POUT(1) = 42W (Typ.)

$$(V_{CC} = 14.4V, f = 1kHz, THD = 10\%, R_L = 2\Omega)$$

: POUT(2) = 37W (Typ.)

$$(V_{CC} = 13.2V, f = 1kHz, THD = 10\%, R_L = 2\Omega)$$

: $P_{OUT}(3) = 22W (Typ.)$

$$(V_{CC} = 13.2V, f = 1kHz, THD = 10\%, R_L = 4\Omega)$$

• Excellent Output Power Bandwidth

:
$$P_{OUT}(4) = 17W \text{ (Typ.)}$$

($V_{CC} = 13.2V, f = 20Hz \sim 20kHz, THD = 1\%, R_L = 4\Omega$)

• Fixed Voltage Gain

:
$$G_V = 32dB (Typ.)$$

$$(V_{CC} = 13.2V, V_{out} = 0.775V_{rms} (0dBm)$$

• Low Thermal Resistance

:
$$\theta_{i-c} = 1.5^{\circ}C/W$$
 (Typ.)

• Low Distortion

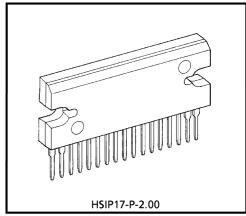
:
$$THD = 0.02\%$$
 (Typ.)

$$(V_{CC} = 13.2V, f = 1kHz, P_{out} = 4W, R_L = 4\Omega)$$

- Built-in Stand-by Switch Function
 - : $I_{STBY} = 100 \mu A (Typ.)$

- Built-in Various Protection Circuit
 - : Thermal Shut Down, Over Voltage, Out to GND, Out to V_{CC}, Out to Out Short
- Operating Supply Voltage

:
$$V_{CC (opr)} = 9 \sim 18V$$

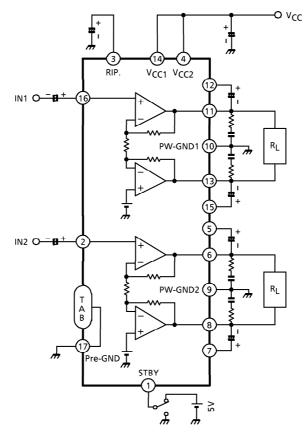


Weight: 9.8g (Typ.)

2001-06-25

BLOCK DIAGRAM

$$(G_V = 32dB)$$



CAUTION AND APPLICATION METHOD

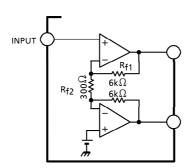
(Description is made only on the single channel.)

1. Voltage gain adjustment

This IC has no NF (negative feedback) terminals. Therefore, the voltage gain can't adjusted, but it makes the device a space and total costs saver.

The voltage gain is decided only internal by expression below.

$$G_V = 20 log \frac{R_{f1}}{R_{f2}} + 6 (dB) = 32 (dB)$$



TOSHIBA TA8231L

2. Stand-By SW Function

By means of controlling pin 1 (Stand-by terminal) to High and Low, the power supply can be set to ON and OFF. The threshold voltage of pin 1 is set at about 3V (Typ.), and the Power Supply current is about $100\,\mu\text{A}$ (Typ.) at the stand-by state.

ON Power 10kΩ ≈ 2VBE

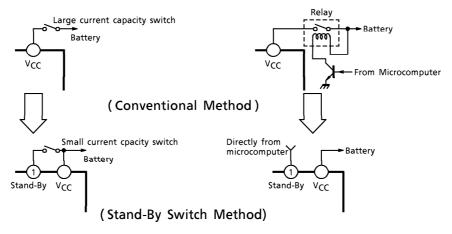
To bias Cutting Circuit

(Fig. 2) With pin① set to High, Power is turned ON.

Pin① Control Voltage: V (SB)

Stand-By	Power	V (SB) (V)
ON	OFF	0~2
OFF	ON	3~V _{CC}

- ~ Advantage of Stand-By SW ~
 - (1) Since V_{CC} can directly be controlled to ON/OFF by the microcomputer, the switching relay can be omitted.
 - (2) Since the control current is microscopic, the switching relay of small current capacity is satisfactory for switching.



3. Preventive measure against oscillation

For preventing the oscillation, it is advisable to use C4, the condenser of polyester film having small characteristic fluctuation of the temperature and the frequency.

The resistance R to be series applied to C4 is effective for phase correction of high frequency, and improves the oscillation allowance.

Since the oscillation allowance is varied according to the causes described below, perform the temperature test to check the oscillation allowance.

- (1) Capacity value of condenser
- (2) Kind of condenser
- (3) Layout of printed board

MAXIMUM RATING (Ta = 25° C)

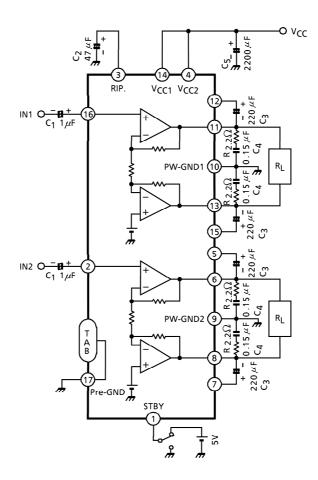
CHARACTERISTIC	SYMBOL	RATING	UNIT
Peak Supply Voltage (0.2s)	V _{CC} (surge)	50	V
DC Supply Voltage	VCC (DC)	25	V
Operating Supply Voltage	V _{CC} (opr)	18	V
Output Current (peak)	I _O (peak)	9	Α
Power Dissipation	PD	50	W
Operating Temperature	T _{opr}	- 30∼85	°C
Storage Temperature	T _{stg}	- 55∼150	°C

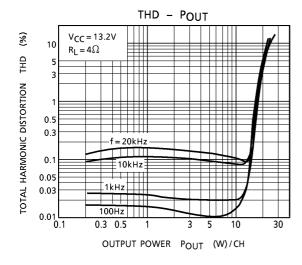
ELECTRICAL CHARACTERISTICS (Unless Otherwise Specified V_{CC} = 13.2V, f = 1kHz, R_g = 600 Ω , R_L = 4 Ω , Ta = 25°C)

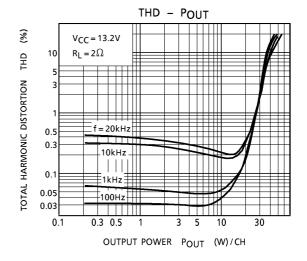
CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	CONDITION	MIN	TYP.	MAX	UNIT
Quiescent Current	I _{ccq}	_	V _{in} = 0	_	150	250	mA
Output Power	Pout (1)	_	V_{CC} = 14.4V, THD = 10%, R_L = 2 Ω		42	_	W
	Pout (2)	_	THD = 10%, $R_L = 2\Omega$	28	37	_	V
	Pout (3)	_	THD = 10%, $R_L = 4\Omega$	19	22	_	8
	Pout (4)	_	THD = 1%, f = 20Hz \sim 20kHz, R _L = 4 Ω		17	_	8
Total Harmonic Distortion	THD	_	P _{out} = 4W	_	0.02	0.2	%
Voltage Gain	GV	_	$V_{out} = 0.775V_{rms}$ (0dBm)	30.5	32	33.5	dB
Voltage Gain Ratio	∆G _V	_	$V_{out} = 0.775V_{rms}$ (0dBm)	- 1	0	1	dB
Output Noise Voltage	V _{NO} (1)	_	$R_g = 0\Omega$, DIN45405 Noise Filter	_	0.07	_	mV _{rms}
	V _{NO} (2)	_	$R_g = 0\Omega$, BW = 20Hz~20kHz	_	0.06	0.1	mV _{rms}
Ripple Rejection Ratio	R.R.	_	f_{ripple} = 100Hz, V_{out} = 0.775 V_{rms} (0dBm) , R_g = 600 Ω	40	60	_	dB
Cross Talk	C.T.	_	$R_g = 600\Omega$, $V_{out} = 0.775V_{rms}$ (0dBm)	_	60	_	dB
Input Resistance	R _{IN}		_		30		kΩ
Output Offset Voltage	Voffset	_	V _{in} = 0	- 200	0	200	mV
Stand-By Current	ISTBY	_	Pin①: GND	_	100	150	μΑ

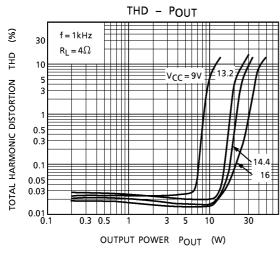
TEST CIRCUIT

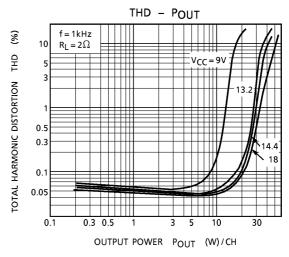
 $(G_V = 32dB)$

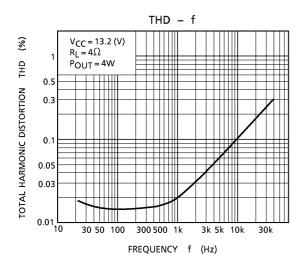


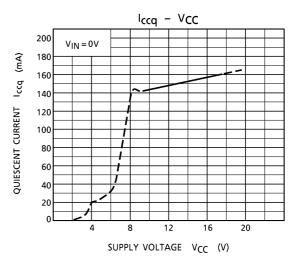


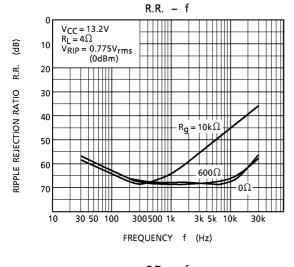


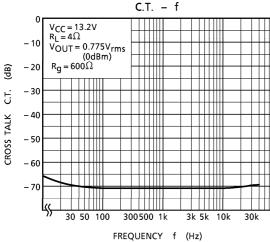


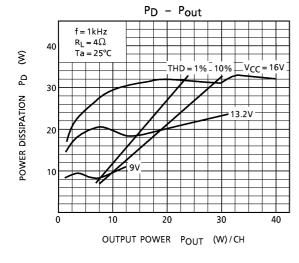


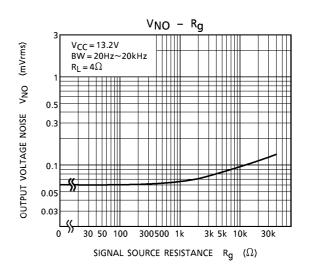


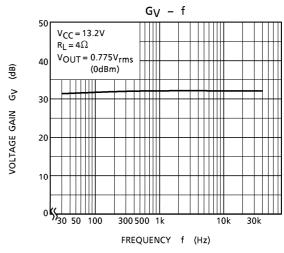


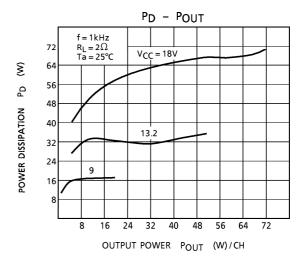


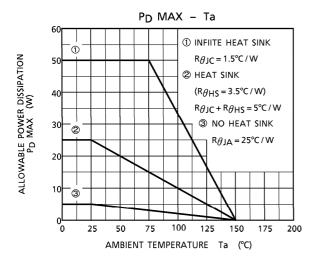






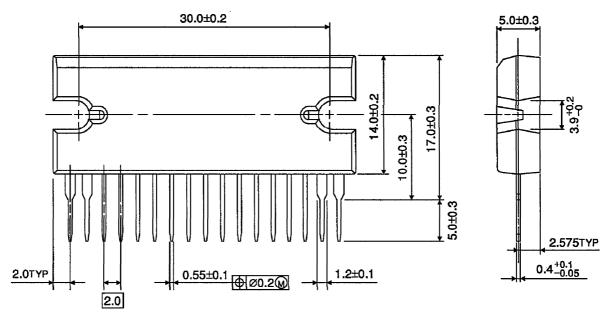


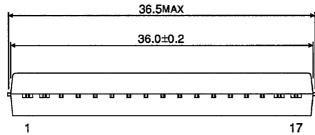




PACKAGE DIMENSIONS

HSIP17-P-2.00 Unit: mm





Weight: 9.8g (Typ.)

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