SIEMENS

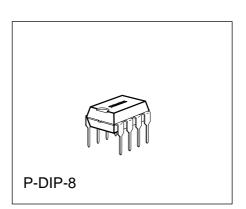
Nonvolatile Memory 1-Kbit E²PROM

SDA 2506-5

Preliminary Data MOS IC

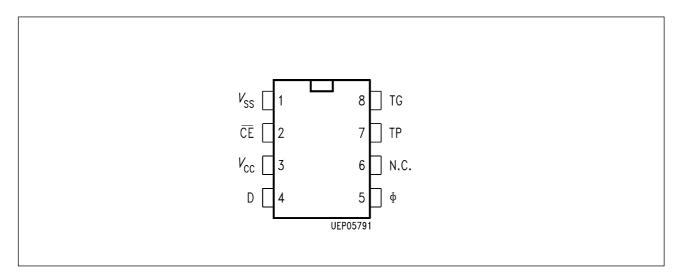
Features

- Word-organized, reprogrammable nonvolatile memory in n-channel floating-gate technology
- 128 × 8-bit organization
- Supply voltage 5 V
- Three lines between control processor and E²PROM for data transfer and chip control
- Serial data bus (eight data bits, seven address bits, one control bit)
- More than 10⁴ reprogramming cycles per address
- Data retention for more than ten years (operatingtemperature range)
- Unlimited number of readout operations without refresh
- Erase or write in 10 ms
- Temperature range 0 to 70 °C



Туре	Ordering Code	Package
SDA 2506-5	Q67100-H5090	P-DIP-8

SIEMENS



Pin Configuration

(top view)

Pin Definitions and Functions

Pin No.	Symbol	Function
1	V_{SS}	Ground
2	CE	CE = 1 for control-world input CE = 0 for reprogramming and data output
3	$V_{\rm CC}$	Supply voltage + 5 V
4	D	Bidirectional data input/output line, for reprogramming: D = 1 erase D = 0 write
5	Φ	Clock
6	N.C.	Not connected (open)
7	TP	Test pin (V_{SS})
8	TG	Test pin (open)