

## Cool MOS Power-Transistor

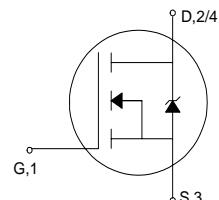
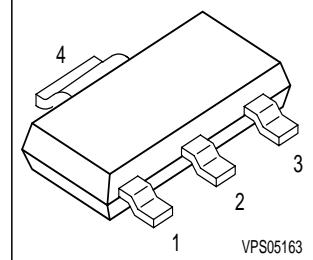
- New revolutionary high voltage technology
- Worldwide best  $R_{DS(on)}$  in SOT 223
- Ultra low gate charge
- Extreme dv/dt rated
- Optimized capacitances
- Improved noise immunity



### Product Summary

$V_{DS} @ T_{jmax}$	650	V
$R_{DS(on)}$	0.95	$\Omega$
$I_D$	0.8	A

SOT-223



Type	Package	Ordering Code	Marking
SPN04N60S5	SOT-223	Q67040-S4211	04N60S5

**Maximum Ratings**, at  $T_j = 25^\circ\text{C}$ , unless otherwise specified

Parameter	Symbol	Value	Unit
Continuous drain current $T_A = 25^\circ\text{C}$	$I_D$	0.8	A
$T_A = 70^\circ\text{C}$		0.65	
Pulsed drain current <sup>1)</sup> $T_A = 25^\circ\text{C}$	$I_D$ puls	3	
Reverse diode dv/dt $I_S = 0.8 \text{ A}, V_{DS} < V_{DSS}, dI/dt = 100 \text{ A}/\mu\text{s}, T_{jmax} = 150^\circ\text{C}$	dv/dt	6	kV/ $\mu\text{s}$
Gate source voltage	$V_{GS}$	$\pm 20$	V
Power dissipation $T_A = 25^\circ\text{C}$	$P_{tot}$	1.8	W
Operating and storage temperature	$T_j, T_{stg}$	-55... +150	$^\circ\text{C}$

**Electrical Characteristics**, at  $T_j = 25^\circ\text{C}$ , unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
<b>Thermal Characteristics</b>					
Thermal resistance, junction - soldering point	$R_{\text{thJS}}$	-	20	-	K/W
SMD version, device on PCB: @ min. footprint @ 6 cm <sup>2</sup> cooling area <sup>2)</sup>	$R_{\text{thJA}}$	-	110	-	K/W
		-	-	70	

**Static Characteristics**, at  $T_j = 25^\circ\text{C}$ , unless otherwise specified

Drain-source breakdown voltage $V_{GS} = 0 \text{ V}, I_D = 0.25 \text{ mA}$	$V_{(\text{BR})DSS}$	600	-	-	V
Gate threshold voltage, $V_{GS} = V_{DS}$ $I_D = 200 \mu\text{A}, T_j = 25^\circ\text{C}$	$V_{GS(\text{th})}$	3.5	4.5	5.5	
Zero gate voltage drain current, $V_{DS}=V_{DSS}$ $V_{GS} = 0 \text{ V}, T_j = 25^\circ\text{C}$ $V_{GS} = 0 \text{ V}, T_j = 150^\circ\text{C}$	$I_{DSS}$	-	0.5	1	$\mu\text{A}$
-	-	-	-	50	
Gate-source leakage current $V_{GS} = 20 \text{ V}, V_{DS} = 0 \text{ V}$	$I_{GSS}$	-	-	100	nA
Drain-source on-state resistance $V_{GS} = 10 \text{ V}, I_D = 0.65 \text{ A}$	$R_{DS(\text{on})}$	-	0.8	0.95	$\Omega$

<sup>1</sup>current limited by  $T_{j\text{max}}$ 
<sup>2</sup> Device on 40mm\*40mm\*1.5mm epoxy PCB FR4 with 6 cm<sup>2</sup> (one layer, 70µm thick) copper area for drain connection. PCB is vertical without blown air.

**Electrical Characteristics**, at  $T_j = 25^\circ\text{C}$ , unless otherwise specified

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
<b>Dynamic Characteristics</b>						
Transconductance	$g_{fs}$	$V_{DS} \geq 2 * I_D * R_{DS(on)max}$ , $I_D = 0.65\text{A}$	-	1	-	S
Input capacitance	$C_{iss}$	$V_{GS}=0\text{V}$ , $V_{DS}=25\text{V}$ , $f=1\text{MHz}$	-	600	-	pF
Output capacitance	$C_{oss}$		-	325	-	
Reverse transfer capacitance	$C_{rss}$		-	15	-	
Turn-on delay time	$t_{d(on)}$	$V_{DD}=350\text{V}$ , $V_{GS}=10\text{V}$ , $I_D=0.8\text{A}$ , $R_G=18\Omega$	-	40	-	ns
Rise time	$t_r$		-	20	-	
Turn-off delay time	$t_{d(off)}$		-	130	-	
Fall time	$t_f$		-	30	-	

### Gate Charge Characteristics

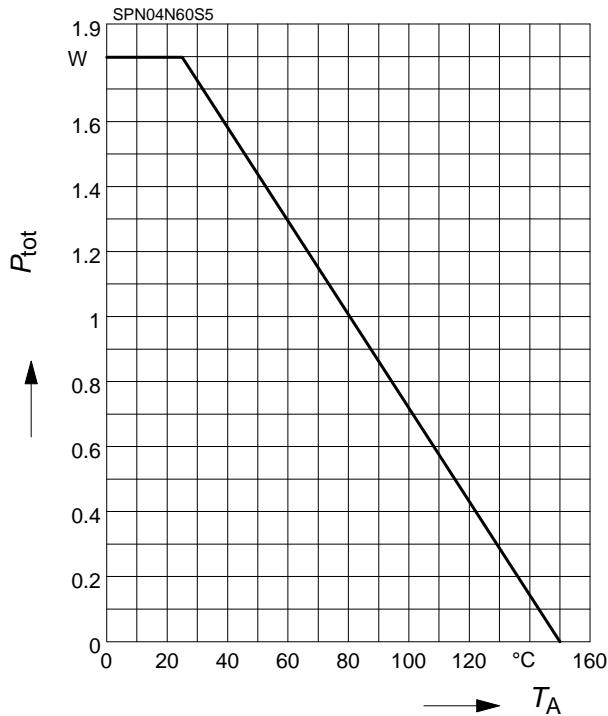
Gate to source charge	$Q_{gs}$	$V_{DD}=350\text{V}$ , $I_D=0.8\text{A}$	-	4.1	-	nC
Gate to drain charge	$Q_{gd}$		-	9.2	-	
Total gate charge	$Q_g$	$V_{DD}=350\text{V}$ , $I_D=0.8\text{A}$ , $V_{GS}=0$ to $10\text{V}$	-	17	-	

### Reverse Diode

Inverse diode continuous forward current	$I_S$	$T_C=25^\circ\text{C}$	-	-	0.8	A
Inverse diode direct current,pulsed	$I_{SM}$		-	-	3	
Inverse diode forward voltage	$V_{SD}$	$V_{GS}=0\text{V}$ , $I_F=0.8\text{A}$	-	0.85	1.05	V
Reverse recovery time	$t_{rr}$	$V_R=350\text{V}$ , $I_F=I_S$ , $dI_F/dt=100\text{A}/\mu\text{s}$	-	200	-	ns
Reverse recovery charge	$Q_{rr}$		-	1.2	-	

### Power Dissipation

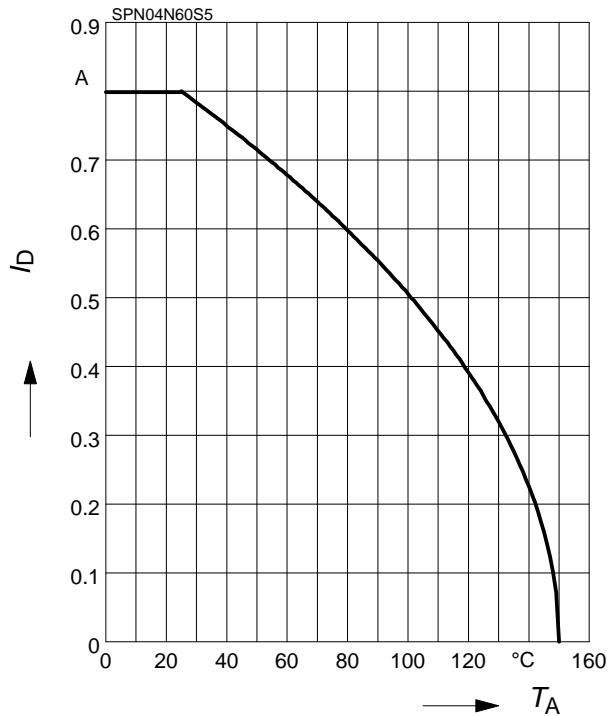
$$P_{\text{tot}} = f(T_A)$$



### Drain current

$$I_D = f(T_A)$$

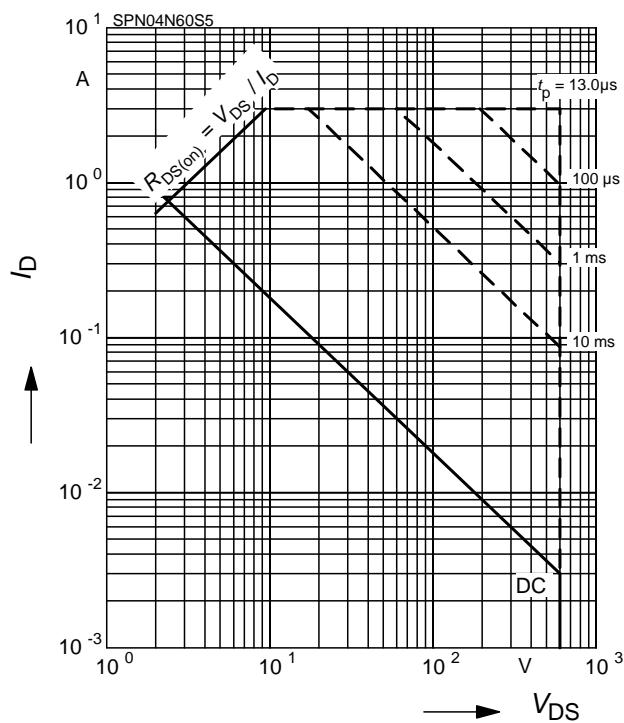
parameter:  $V_{GS} \geq 10$  V



### Safe operating area

$$I_D = f(V_{DS})$$

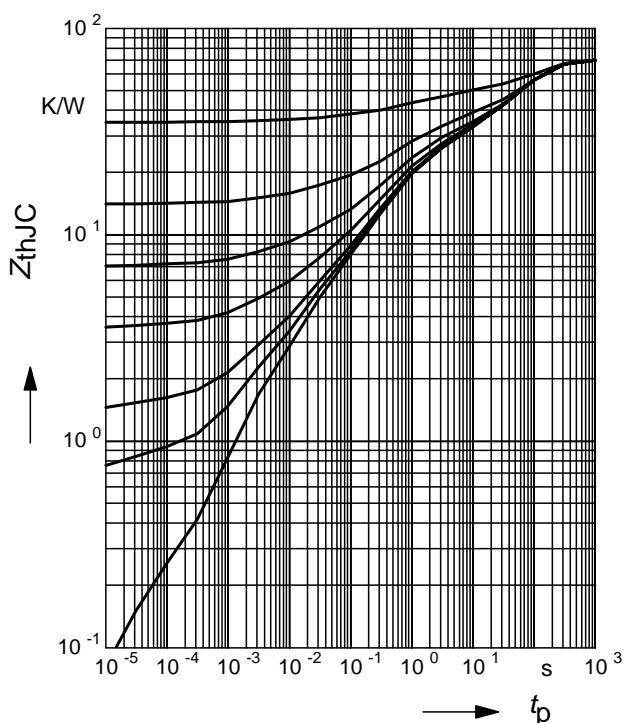
parameter:  $D=0.01$ ,  $T_A=25^\circ\text{C}$



### Transient thermal impedance

$$Z_{\text{thJS}} = f(t_p)$$

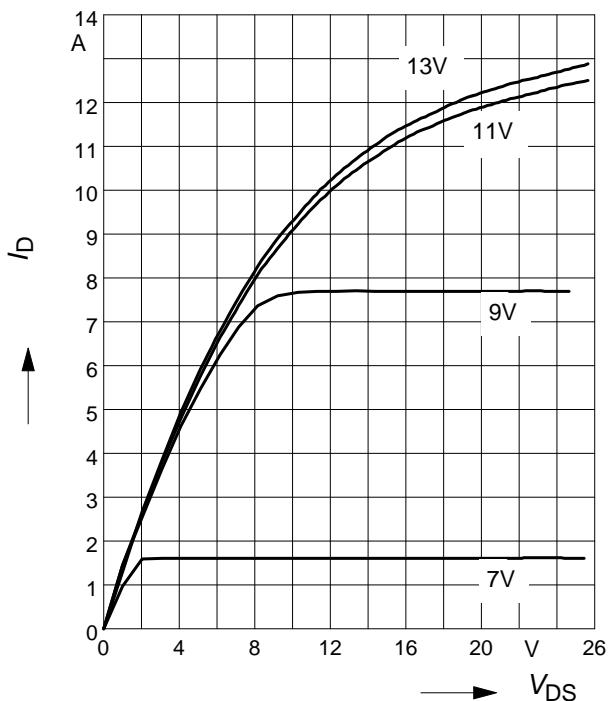
Parameter:  $D=t_p/T$



### Typ. output characteristic

$$I_D = f(V_{DS})$$

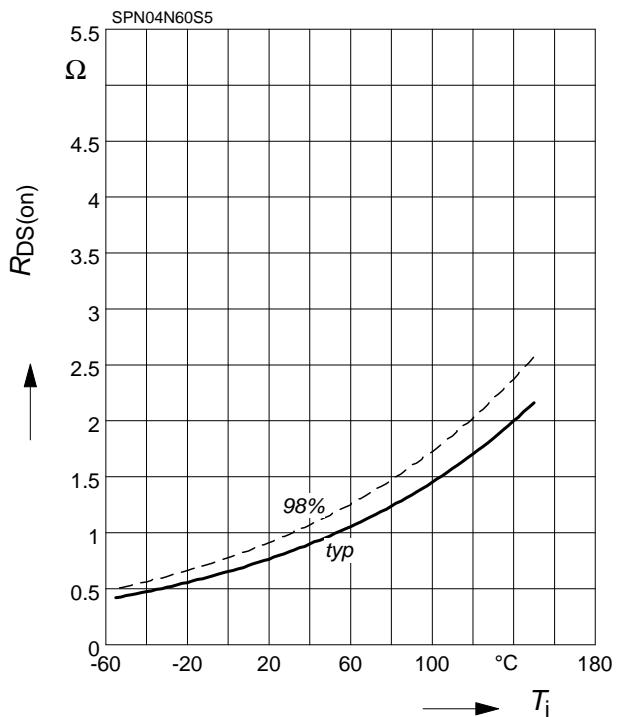
Parameter:  $V_{GS}$ ,  $T_j = 25^\circ\text{C}$



### Drain-source on-resistance

$$R_{DS(on)} = f(T_j)$$

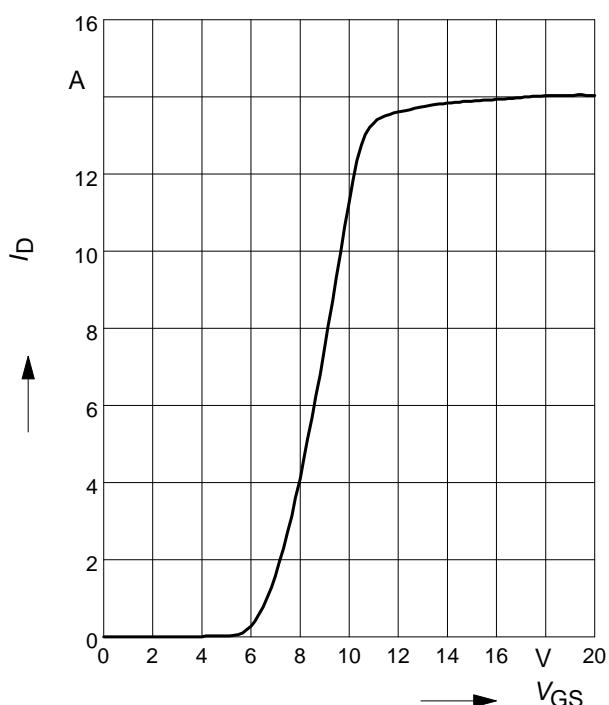
parameter :  $I_D = 0.65 \text{ A}$ ,  $V_{GS} = 10 \text{ V}$



### Typ. transfer characteristics

$$I_D = f(V_{GS})$$

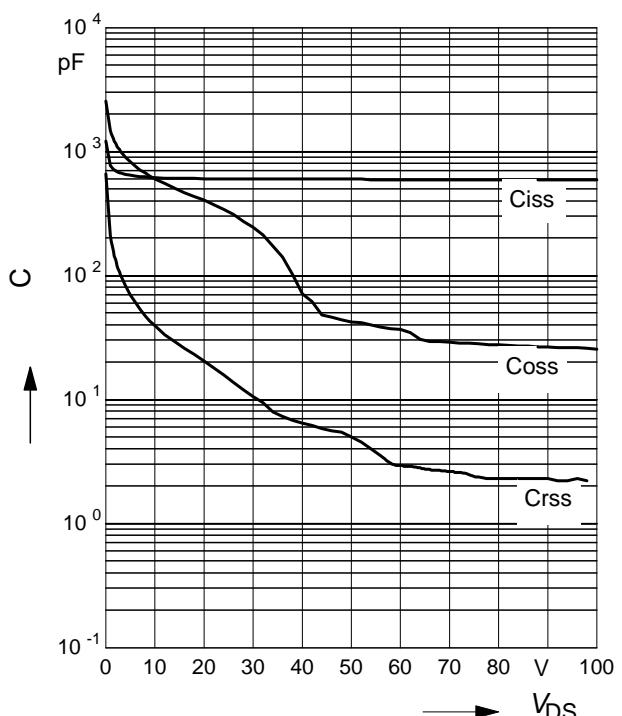
$V_{DS} \geq 2 \times I_D \times R_{DS(on)\max}$



### Typ. capacitances

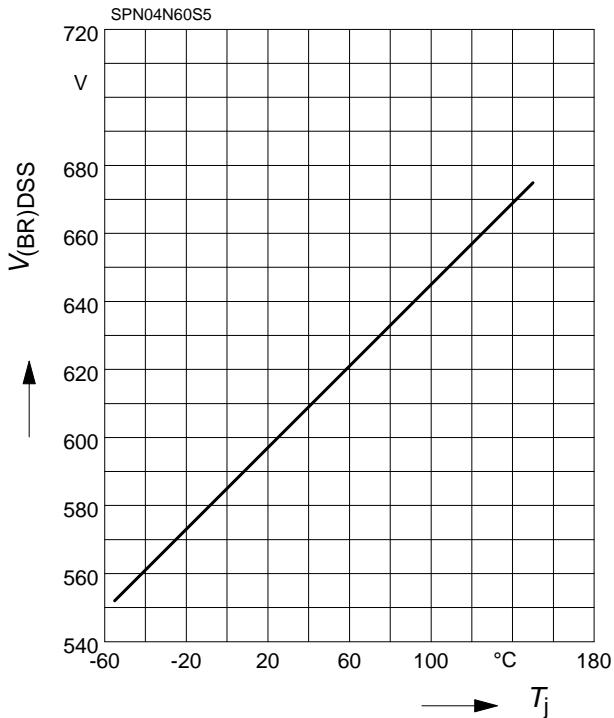
$$C = f(V_{DS})$$

parameter:  $V_{GS}=0 \text{ V}$ ,  $f=1 \text{ MHz}$

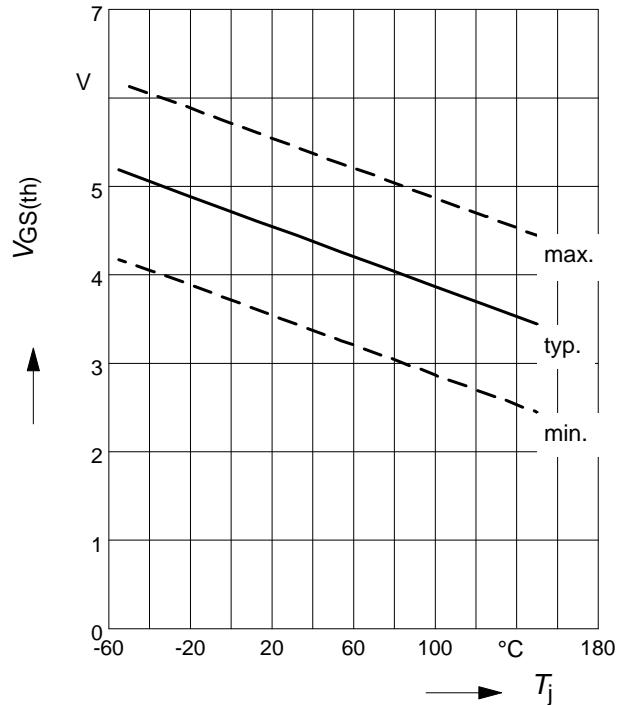


**Drain-source breakdown voltage**

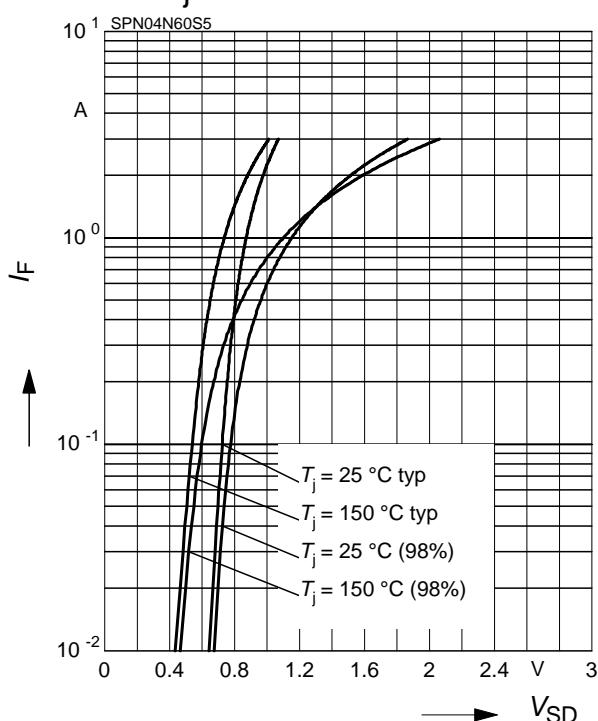
$$V_{(BR)DSS} = f(T_j)$$


**Gate threshold voltage**

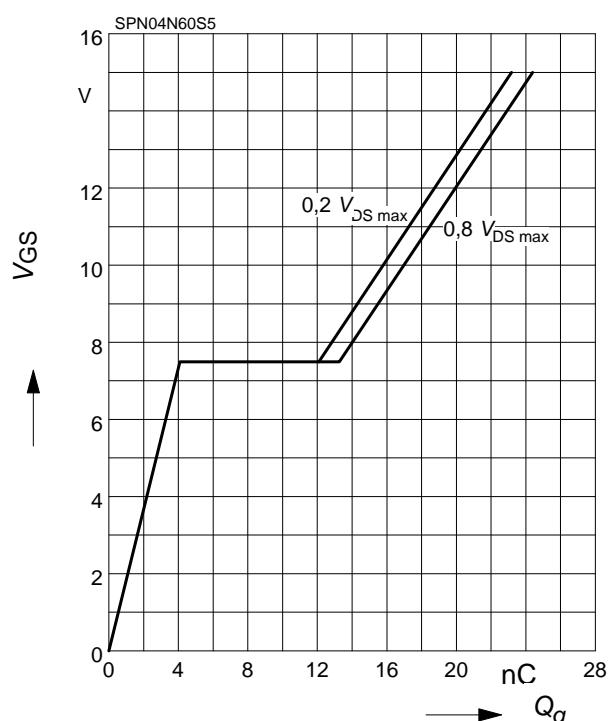
$$V_{GS(th)} = f(T_j)$$

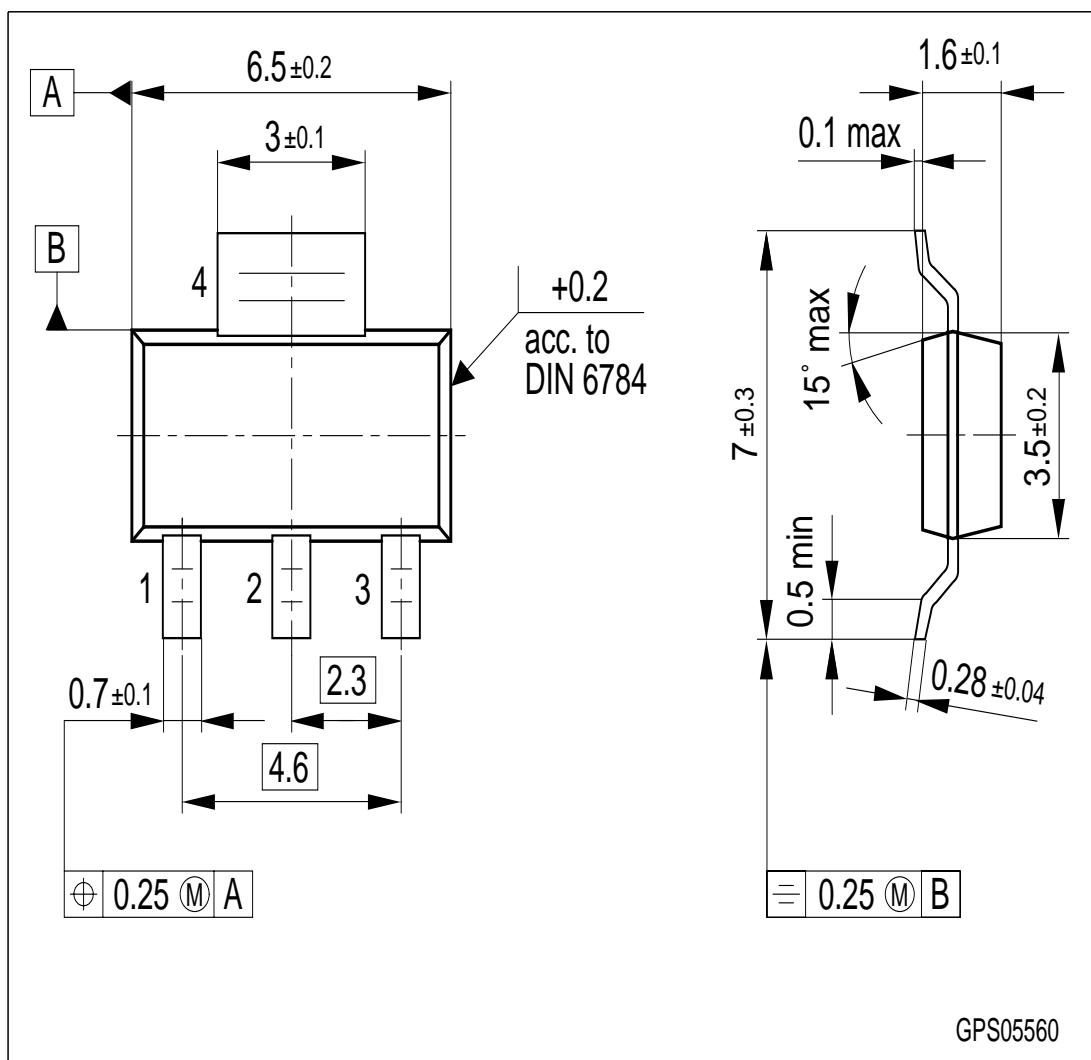
 parameter:  $V_{GS} = V_{DS}$ ,  $I_D = 200 \mu\text{A}$ 

**Forward characteristics of reverse diode**

$$I_F = f(V_{SD})$$

 parameter:  $T_j$ ,  $t_p = 10 \mu\text{s}$ 

**Typ. gate charge**

$$V_{GS} = f(Q_{Gate})$$

 parameter:  $I_D = 0.8 \text{ A pulsed}$ 




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