

MN3207

1024-STAGE LOW VOLTAGE OPERATION LOW NOISE BBD

General description

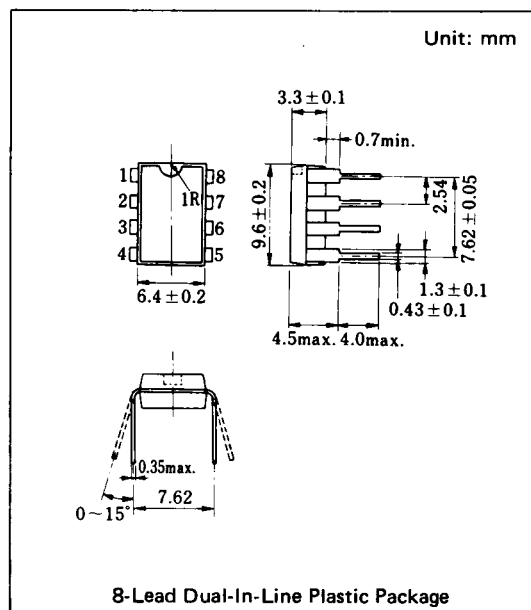
The MN3207 is a 1024-stage long delay low noise BBD that provides a signal delay of up to 51.2ms and is particularly suitable as a device for generation of reverberation effect in audio equipment such as low voltage operation portable stereo and radio cassette recorders.

Features

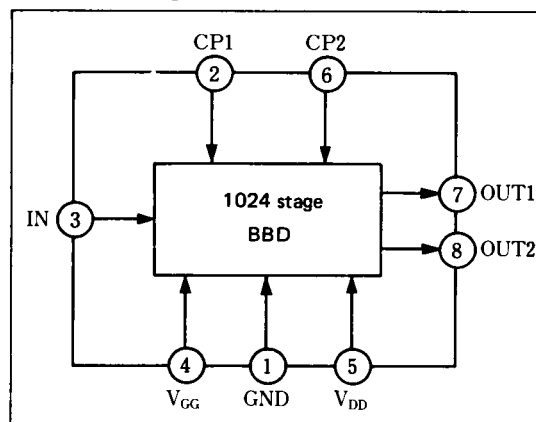
- Variable delay of audio signals: 2.56ms ~ 51.2ms.
- Wide supply voltage: 4 ~ 10V.
- No insertion loss: $L_i = 0\text{dB typ.}$
- Wide dynamic range: $S/N = 73\text{dB typ.}$
- Low distortion: $\text{THD} = 0.4\% \text{ typ. } (V_i = 0.25\text{Vrms}).$
- Clock frequency range: 10KHz ~ 200KHz.
- N-channel silicon gate process.
- 8-lead dual-in-line plastic package.

Applications

- Reverberation and echo effects of audio equipment such as radio cassette recorder, car radio, portable radio, portable stereo, echo microphone and pre-taped musical accompaniment (Karaoke), etc.
- Sound effect in electronic musical instruments.
- Variable or fixed delay of analog signals.



Block Diagram



Quick Reference Data

Item	Symbol	Value	Unit
Supply Voltage	V_{DD}, V_{GG}	$+5, \frac{1}{3}V_{DD}$	V
Signal Delay Time	t_D	2.56~51.2	ms
Total Harmonic Distortion	THD	0.4	%
Signal to Noise Ratio	S/N	73	dB

■ Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Rating	Unit
Terminal Voltage	$V_{DD}, V_{GG}, V_{CP}, V_I$	-0.3~+11	V
Output Voltage	V_O	-0.3~+11	V
Operating Temperature	Topr	-20~+60	°C
Storage Temperature	Tstg	-55~+125	°C

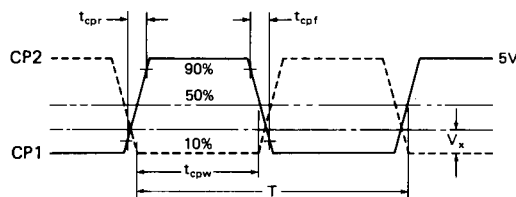
■ Operating Condition (Ta = 25°C)

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Drain Supply Voltage	V_{DD}		+4	+5	+10	V
Gate Supply Voltage	V_{GG}			$\frac{14}{15}V_{DD}$		V
Clock Voltage "H" Level	V_{CPH}			V_{DD}		V
Clock Voltage "L" Level	V_{CPL}		0		+1	V
Clock frequency	f_{CP}		10		200	kHz
Clock Pulse Width *1	t_{CPW}				$0.5T$ *2	
Clock Rise Time *1	t_{CPr}				500	ns
Clock Fall Time *1	t_{CPf}				500	ns
Clock Input Capacitance	C_{CP}				700	pF
Clock Cross Point *1	V_X		0		$0.3V_{CPH}$	V

■ Electrical Characteristics (Ta = 25°C, $V_{DD} = V_{CPH} = 5V$, $V_{CPL} = 0V$, $V_{GG} = 4.67V$, $R_L = 100k\Omega$)

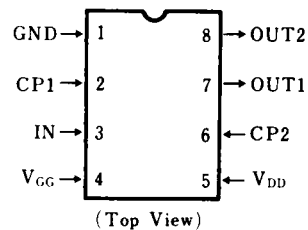
Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Signal Delay Time	t_D		2.56		51.2	ms
Input Signal Frequency	f_i	$f_{CP} = 40kHz$, $V_i = 0.35V_{rms}$ 3dB down (0dB at $f_i = 1kHz$) = 1kHz	10			kHz
Input Signal Swing	V_i	$f_{CP} = 40kHz$, $f_i = 1kHz$, THD=2.5%	0.36			Vrms
Insertion Loss	L_i	$f_{CP} = 40kHz$, $f_i = 1kHz$, $V_i = 0.36V_{rms}$	-4	0	4	dB
Total Harmonic Distortion	THD	$f_{CP} = 40kHz$, $f_i = 1kHz$, $V_i = 0.25V_{rms}$		0.4	2.5	%
Noise Voltage	V_{no}	$f_{CP} = 100kHz$, Weighted by "A" curve			0.25	mVrms
Signal to Noise Ratio	S/N			73		dB

*1 Clock Pulse Waveforms

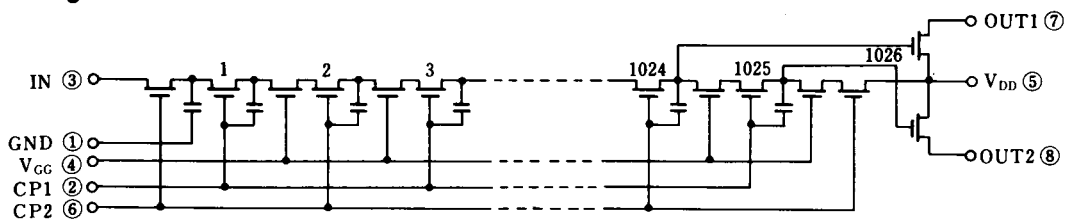


*2 $T = 1/f_{CP}$ (Clock Period)

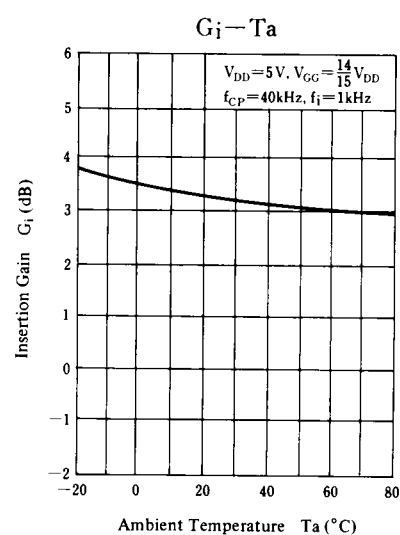
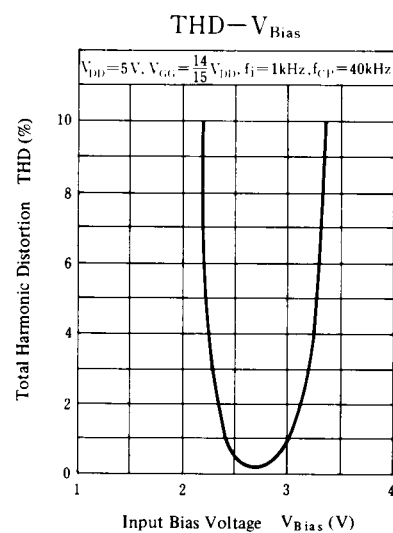
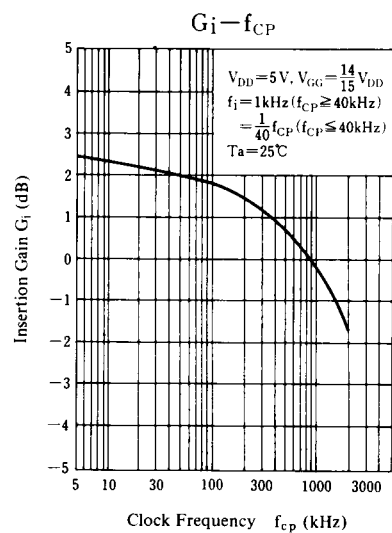
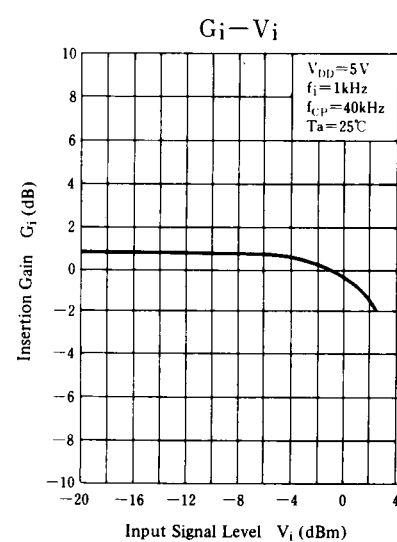
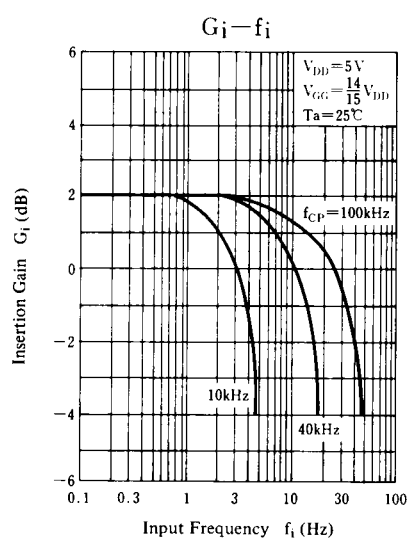
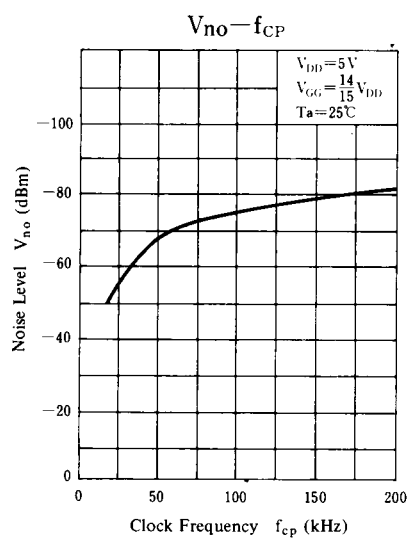
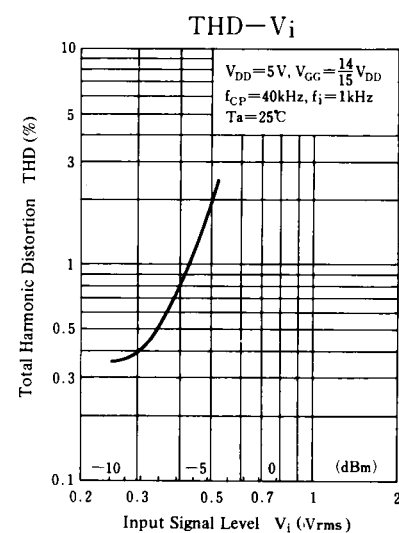
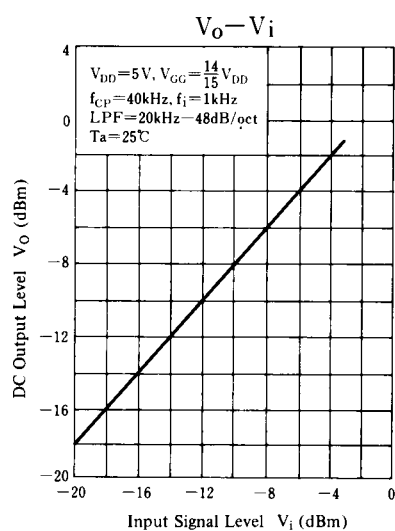
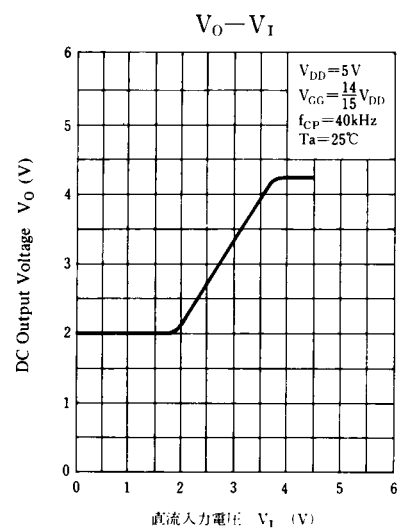
■ Terminal Assignments

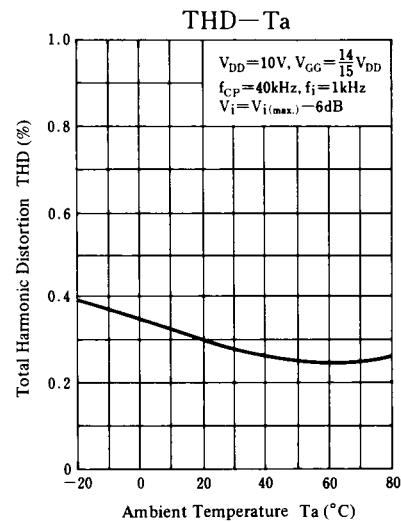
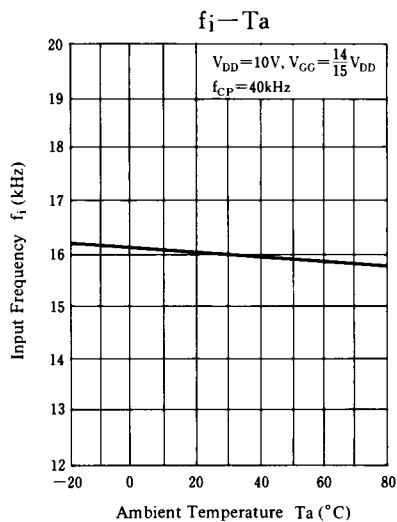
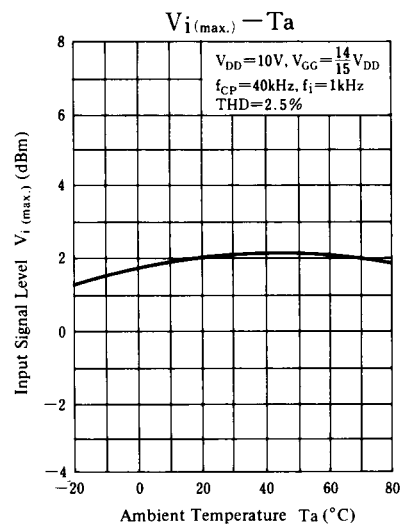


■ Circuit Diagram

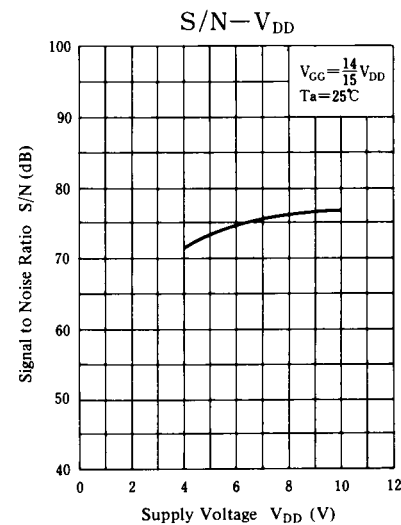
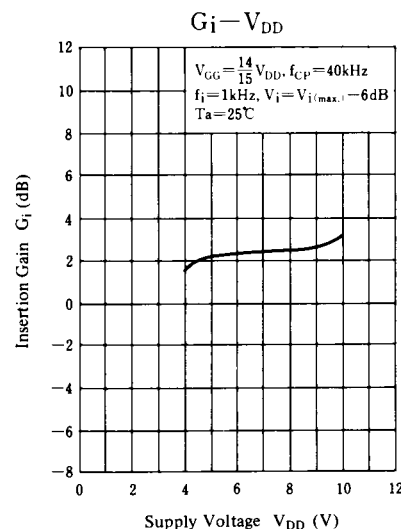
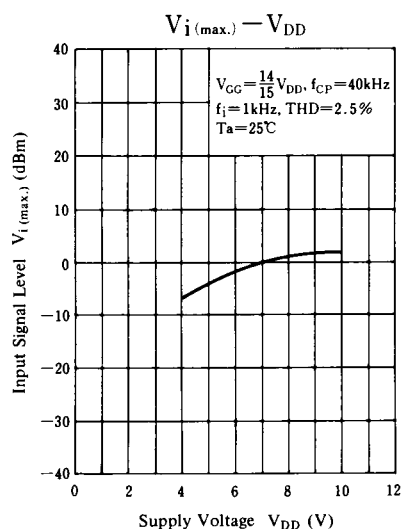
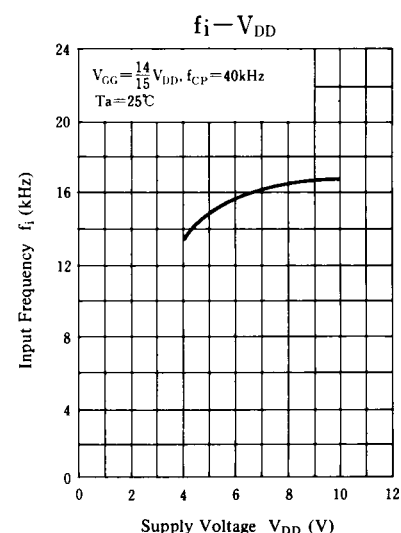
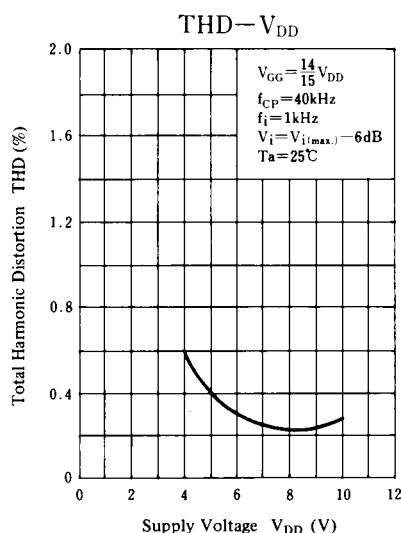
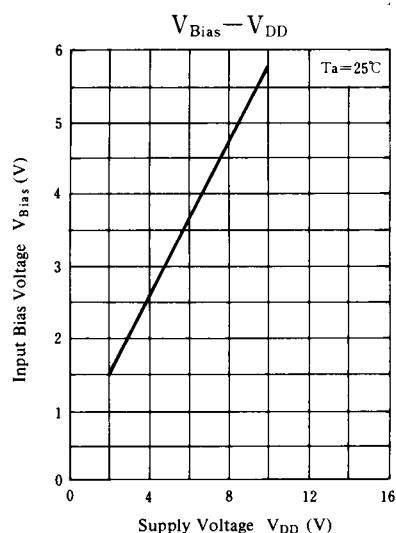


■ Typical Electrical Characteristic Curves

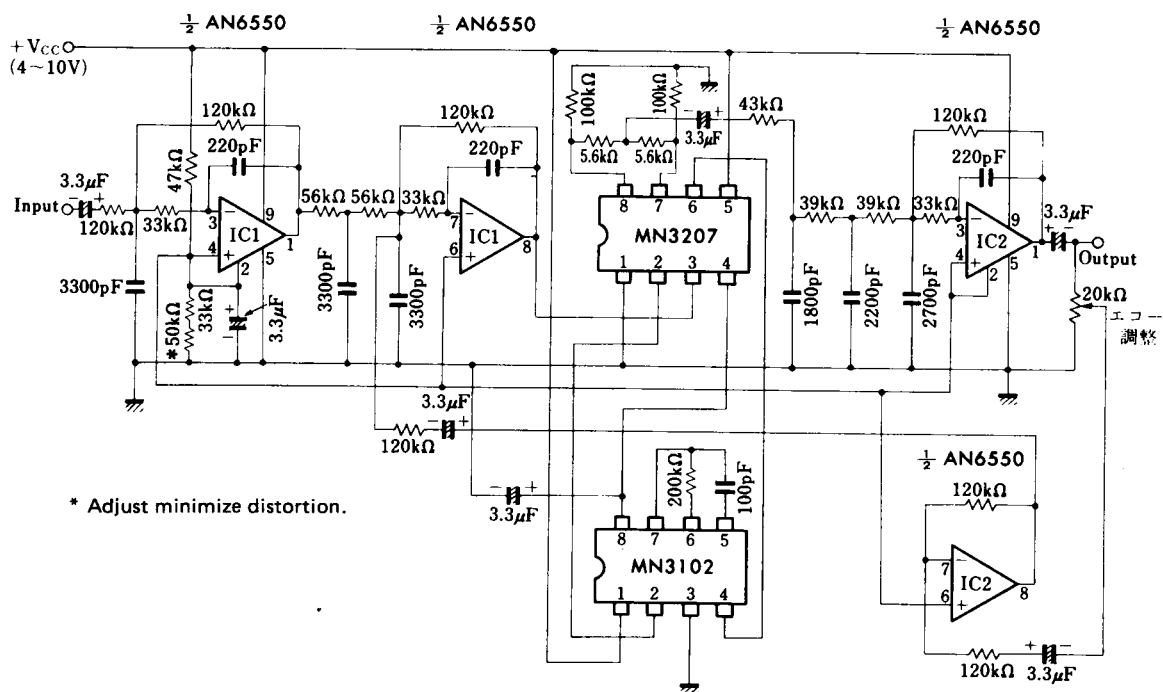




Supply Voltage Characteristics



■ Application Circuit



* Adjust minimize distortion.

Echo Effect Generation Circuit