

## LM358

## LINEAR INTEGRATED CIRCUIT

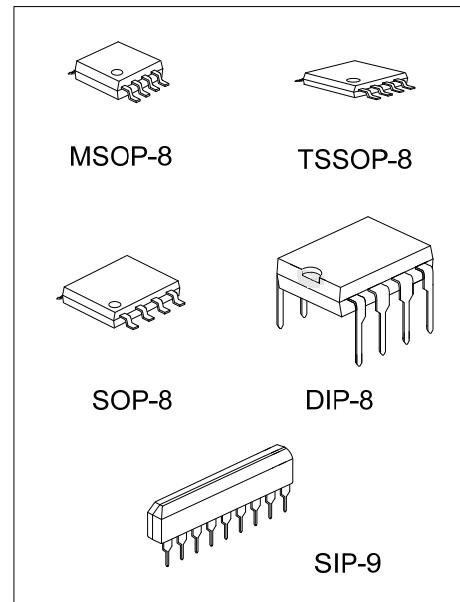
## DUAL OPERATIONAL AMPLIFIER

## ■ DESCRIPTION

The UTC **LM358** consists of two independent high gain, internally frequency compensated operational amplifier. It can be operated from a single power supply and also split power supplies.

## ■ FEATURES

- \*Internally frequency compensated for unity gain.
- \*Wide power supply range 3V - 32V.
- \*Input common-mode voltage range include ground.
- \*Large DC voltage gain.

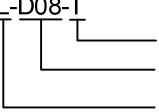


Lead-free: LM358L

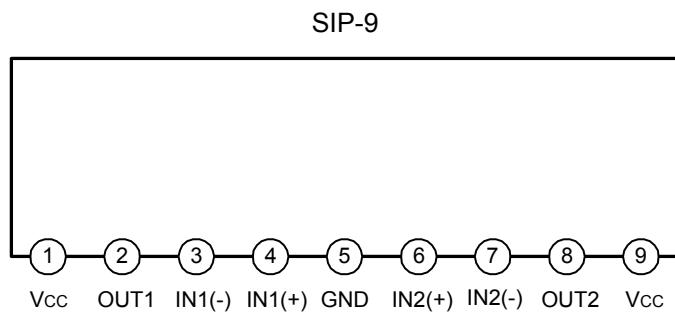
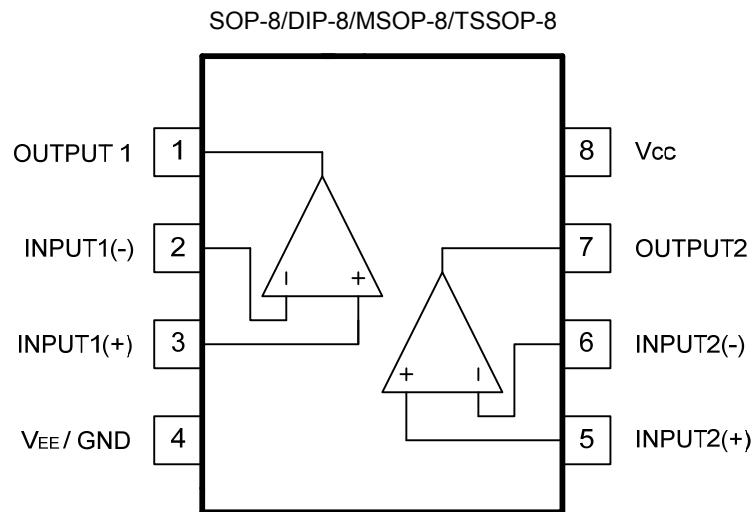
Halogen-free: LM358G

## ■ ORDERING INFORMATION

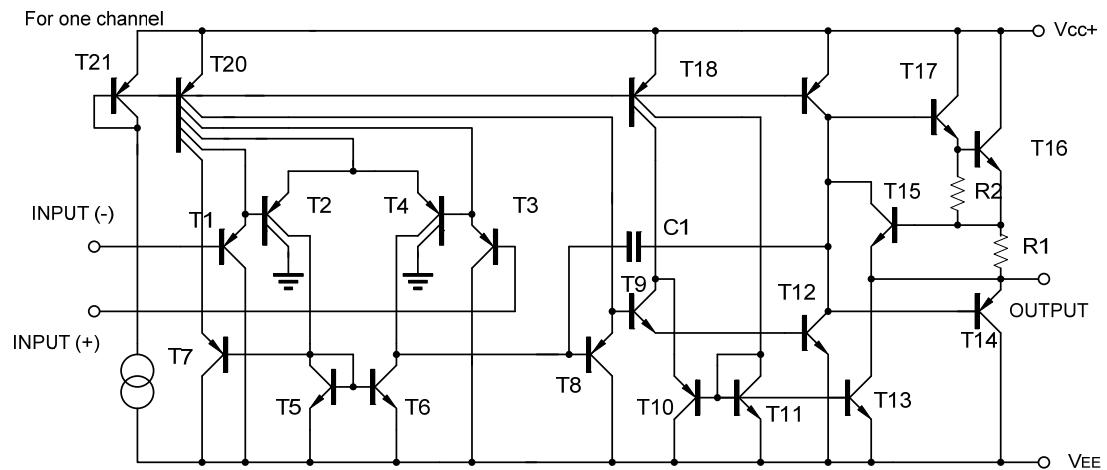
Ordering Number			Package	Packing
Normal	Lead Free Plating	Halogen-Free		
LM358-D08-T	LM358L-D08-T	LM358G-D08-T	DIP-8	Tube
LM358-G09-T	LM358L-G09-T	LM358G-G09-T	SIP-9	Tube
LM358-P08-R	LM358L-P08-R	LM358G-P08-R	TSSOP-8	Tape Reel
LM358-P08-T	LM358L-P08-T	LM358G-P08-T	TSSOP-8	Tube
LM358-S08-R	LM358L-S08-R	LM358G-S08-R	SOP-8	Tape Reel
LM358-S08-T	LM358L-S08-T	LM358G-S08-T	SOP-8	Tube
LM358-SM1-R	LM358L-SM1-R	LM358G-SM1-R	MSOP-8	Tape Reel
LM358-SM1-T	LM358L-SM1-T	LM358G-SM1-T	MSOP-8	Tube

 (1)Packing Type (2)Package Type (3)Lead Plating	(1) R: Tape Reel, T: Tube (2) D08: DIP-8, G09: SIP-9, S08: SOP-8, SM1: MSOP-8, P08: TSSOP-8 (3) G: Halogen Free, L: Lead Free Plating, Blank: Pb/Sn
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### ■ PIN DESCRIPTION



### ■ BLOCK DIAGRAM



■ ABSOLUTE MAXIMUM RATINGS

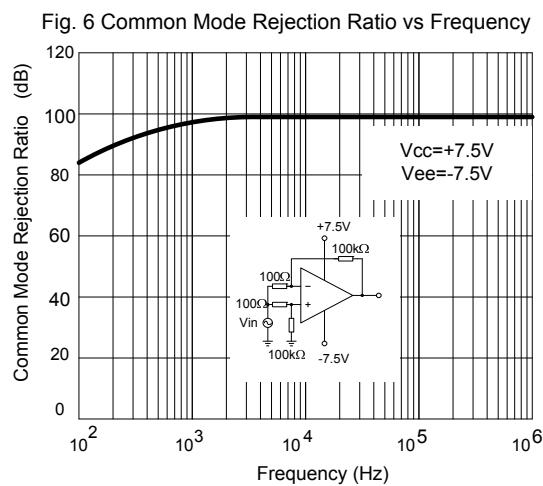
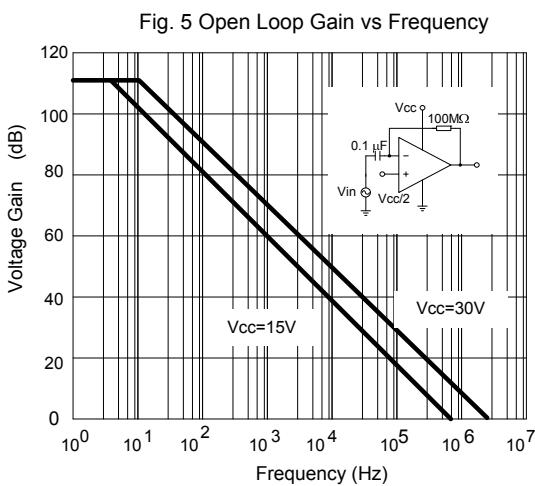
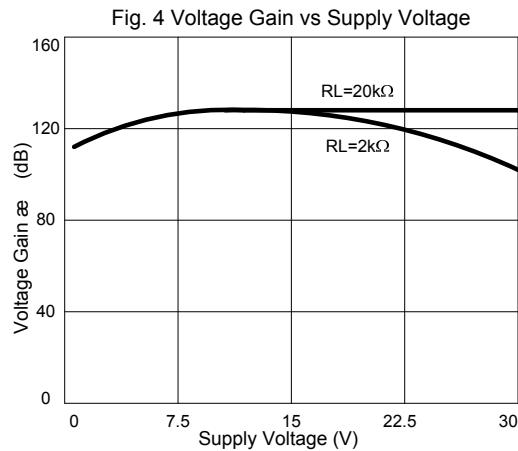
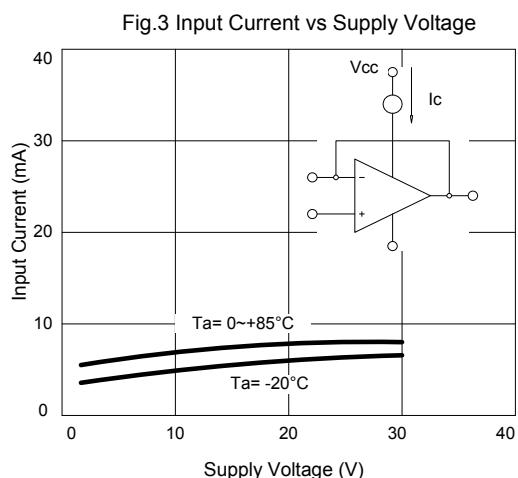
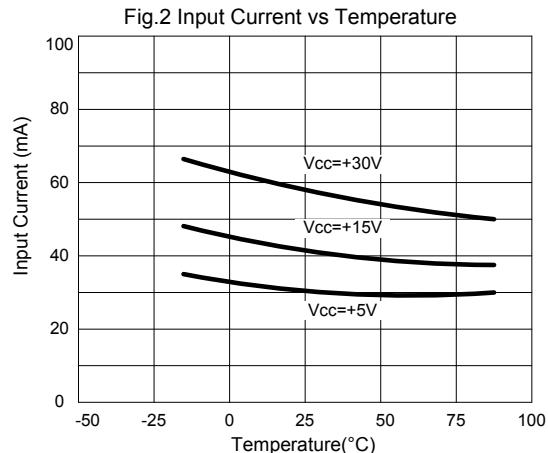
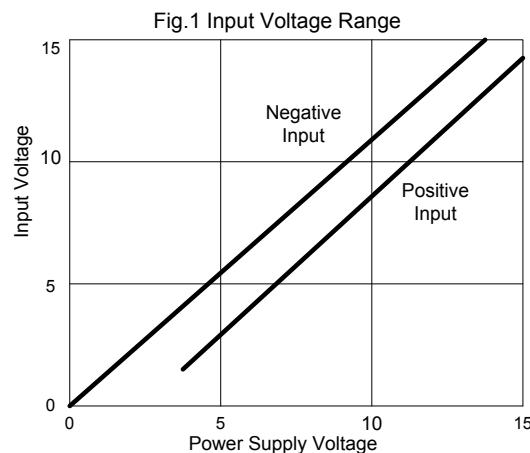
PARAMETER		SYMBOL	RATINGS	UNIT
Supply Voltage		V <sub>CC</sub>	±16 or 32	V
Differential Input Voltage		V <sub>I(DIFF)</sub>	±32	V
Input Voltage		V <sub>I</sub>	-0.3 ~ +32	V
Output Short to Ground			Continuous	
Power Dissipation	SIP-9	P <sub>D</sub>	600	mW
	DIP-8		500	
	SOP-8		280	
	TSSOP-8/MSOP-8		200	
Junction Temperature		T <sub>J</sub>	+125	°C
Operating Temperature		T <sub>OPR</sub>	-40 ~ +85	°C
Storage Temperature		T <sub>STG</sub>	-65 ~ +150	°C

Note Absolute maximum ratings are those values beyond which the device could be permanently damaged.  
Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ ELECTRICAL CHARACTERISTICS (V<sub>CC</sub>=5.0V, V<sub>EE</sub>=GND, Ta=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Input Offset Voltage	V <sub>I(OFF)</sub>	V <sub>CM</sub> =0V to V <sub>CC</sub> -1.5V V <sub>O(P)</sub> =1.4V, R <sub>S</sub> =0Ω		2.9	7.0	mV
Input Common Mode Voltage	V <sub>I(CM)</sub>	V <sub>CC</sub> =30V	0		V <sub>CC</sub> -1.5	V
Differential Input Voltage	V <sub>I(DIFF)</sub>				V <sub>CC</sub>	V
Output Voltage Swing	V <sub>OH</sub>	V <sub>CC</sub> =30V, R <sub>L</sub> =2KΩ	26			V
		V <sub>CC</sub> =30V, R <sub>L</sub> =10KΩ	27	28		V
Large Signal Voltage Gain	G <sub>V</sub>	V <sub>CC</sub> =5V, R <sub>L</sub> ≥10KΩ		5	20	mV
		V <sub>CC</sub> =15V, R <sub>L</sub> ≥2KΩ V <sub>O(P)</sub> =1V ~ 11V	25	100		V/mV
Power Supply Current	I <sub>CC</sub>	R <sub>L</sub> =∞, V <sub>CC</sub> =30V		0.8	2.0	mA
		R <sub>L</sub> =∞, Full Temperature Range		0.5	1.2	mA
Input Offset Current	I <sub>I(OFF)</sub>			5	50	nA
Input Bias Current	I <sub>I(BIAS)</sub>			45	250	nA
Short Circuit Current to Ground	I <sub>SC</sub>			40	60	mA
Output Current	I <sub>SOURCE</sub>	V <sub>I(+)</sub> =1V, V <sub>I(-)</sub> =0V V <sub>CC</sub> =15V, V <sub>O(P)</sub> =2V	10	30		mA
	I <sub>SINK</sub>	V <sub>I(+)</sub> =0V, V <sub>I(-)</sub> =1V V <sub>CC</sub> =15V, V <sub>O(P)</sub> =2V	10	15		mA
		V <sub>I(+)</sub> =0V, V <sub>I(-)</sub> =1V V <sub>CC</sub> =15V, V <sub>O(P)</sub> =200mV	12	100		μA
Common Mode Rejection Ratio	CMRR		65	80		dB
Power Supply Rejection Ratio	PSRR		65	100		dB
Channel Separation	CS	f=1KHZ ~ 20KHZ		120		dB

### ■ TYPICAL CHARACTERISTICS



### ■ TYPICAL CHARACTERISTICS(Cont.)

Fig. 7 Voltage Follower Pulse Response

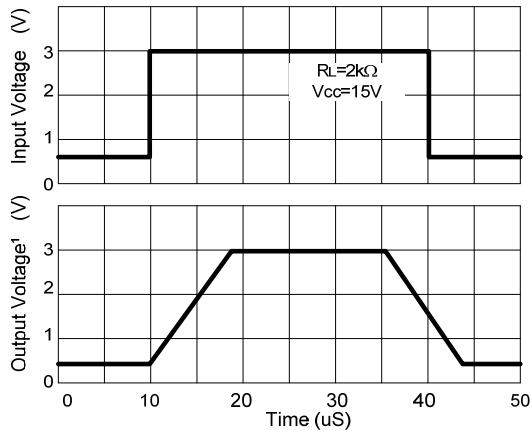


Fig. 8 Voltage Follower Response (Small Signal)

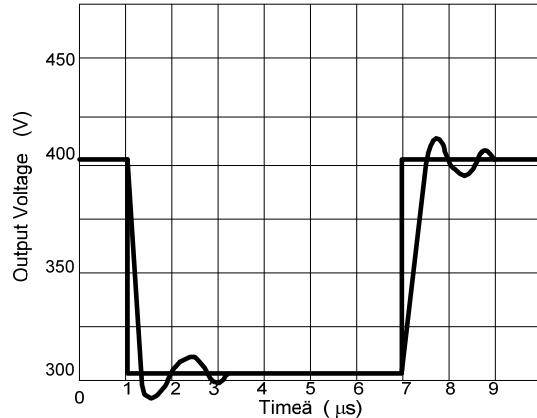


Fig. 9 Gain vs Large Signal Frequency

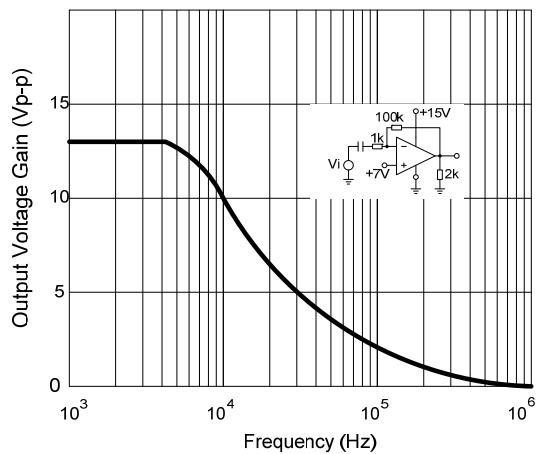


Fig. 10 Output Current Sinking vs Output Voltage

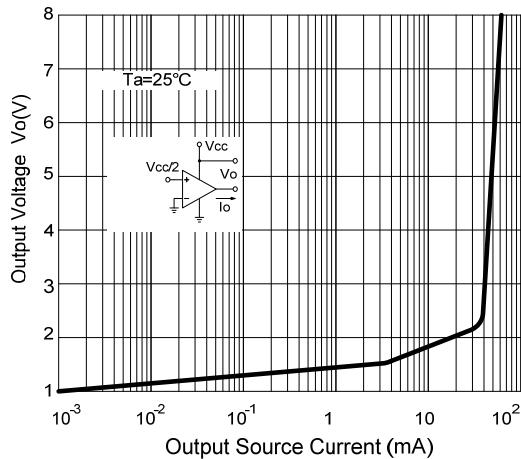


Fig. 11 Output Sink Current vs Output Voltage

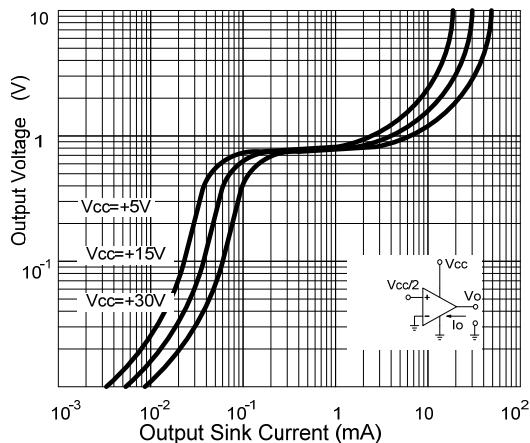
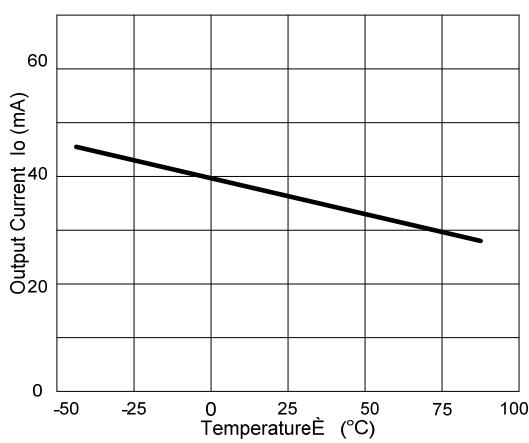


Fig. 12 Current Limiting vs Temperature



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