TOSHIBA Insulated Gate Bipolar Transistor Silicon N Channel IGBT

GT40T301

Parallel Resonance Inverter Switching Applications

Unit: mm

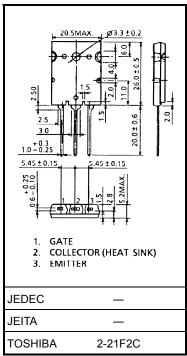
- FRD included between emitter and collector
- Enhancement mode type
- High speed IGBT: $t_f = 0.25 \mu s$ (typ.) (IC = 40 A)

FRD : $t_{rr} = 0.7 \mu s$ (typ.) (di/dt = -20 A/ μs)

• Low saturation voltage: VCE (sat) = 3.7 V (typ.) (IC = 40 A)

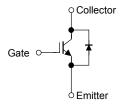
Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Collector-emitter voltage		V _{CES}	1500	V	
Gate-emitter voltage		V _{GES}	±25	V	
Collector current	DC	I _C	40	А	
	1 ms	I _{CP}	80		
Emitter-collector forward current	DC	I _{ECF}	30	A	
	1 ms	I _{ECPF}	80		
Collector power dissipation (Tc = 25°C)		P _C	200	W	
Junction temperature	Tj	150	°C		
Storage temperature range		T _{stg}	−55~150	°C	

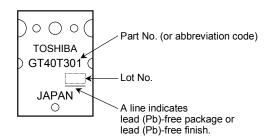


Weight: 9.75 g (typ.)

Equivalent Circuit



Marking

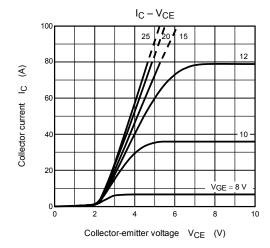


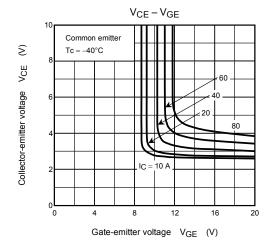
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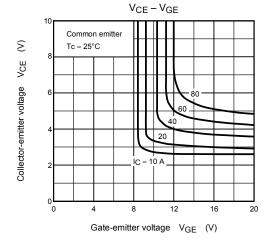
Electrical Characteristics (Ta = 25°C)

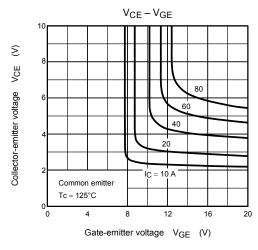
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GES}	$V_{GE} = \pm 25 \text{ V}, V_{CE} = 0$	_	_	±500	nA
Collector cut-off current		I _{CES}	V _{CE} = 1500 V, V _{GE} = 0	_	_	1.0	mA
Gate-emitter cut-off voltage		V _{GE} (OFF)	$I_C = 40$ mA, $V_{CE} = 5$ V	4.0	_	7.0	V
Collector-emitter saturation voltage		V _{CE (sat)}	I _C = 40 A, V _{GE} = 15 V	_	3.7	5.0	V
Input capacitance		C _{ies}	$V_{CE} = 10 \text{ V}, V_{GE} = 0, f = 1 \text{ MHz}$	_	2900	_	pF
Switching time	Rise time	t _r	51 Ω G G S S S S S S S S S S S S S S S S S		0.40	_	μs
	Turn-on time	t _{on}			0.45		
	Fall time	t _f			0.23	0.40	
	Turn-off time	t _{off}		_	0.6		
Emitter-collector forward voltage		V _{ECF}	I _{ECF} = 30 A, V _{GE} = 0	_	1.9	2.5	V
Reverse recovery time		t _{rr}	$I_{ECF}=30~A,~V_{GE}=0,~di/dt=-20~A/\mu s$	_	0.7	3.0	μS
Thermal resistance		R _{th (j-c)}	IGBT	_	_	0.625	°C/W
			Diode	_	_	1.25	

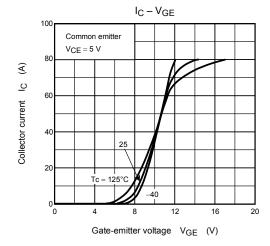
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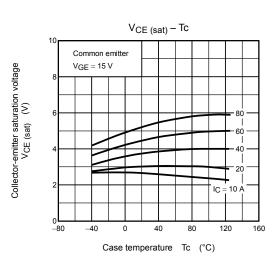


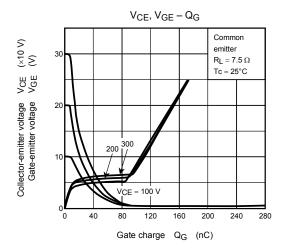


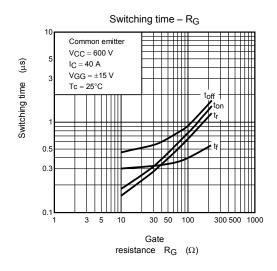


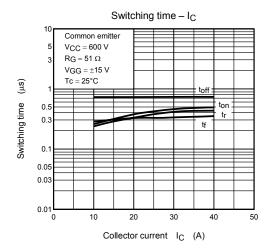


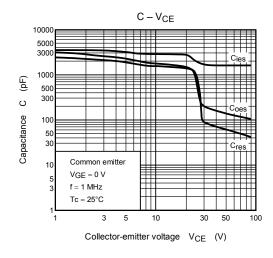


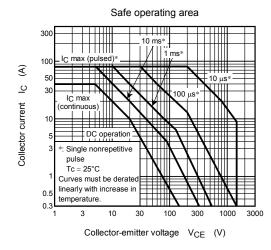


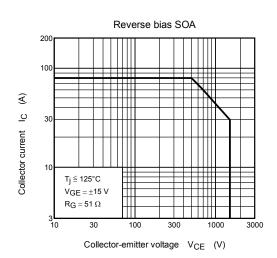


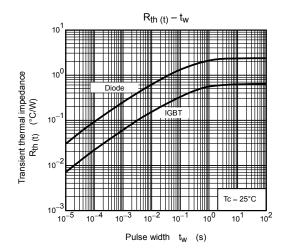


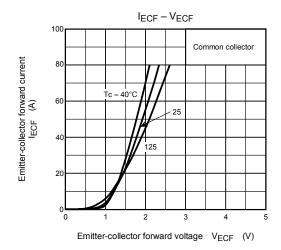


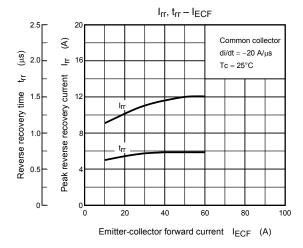


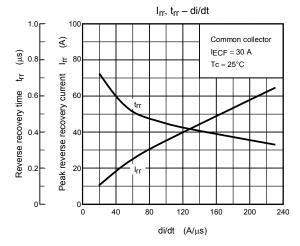












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