SDAS142C - JULY 1987 - REVISED AUGUST 1995

- 3-State Outputs Drive Bus Lines or Buffer Memory Address Registers
- pnp Inputs Reduce dc Loading
- Package Options Include Plastic Small-Outline (DW) Packages, Ceramic Chip Carriers (FK), and Standard Plastic (N) and Ceramic (J) 300-mil DIPs

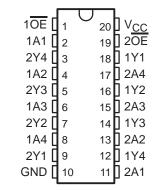
description

These octal buffers and line drivers are designed specifically to improve the performance and density of 3-state memory address drivers, clock drivers, and bus-oriented receivers and transmitters. With the 'ALS240A, 'ALS241C, 'AS240A, and 'AS241A, these devices provide the choice of selected combinations of inverting outputs, symmetrical active-low output-enable (\overline{OE}) inputs, and complementary \overline{OE} and \overline{OE} inputs.

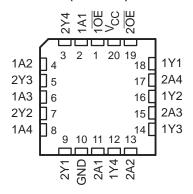
The -1 version of SN74ALS244C is identical to the standard version, except that the recommended maximum I_{OL} for the -1 version is 48 mA. There is no -1 version of the SN54ALS244C.

The SN54ALS244C and SN54AS244A are characterized for operation over the full military temperature range of -55°C to 125°C. The SN74ALS244C and SN74AS244A are characterized for operation from 0°C to 70°C.

SN54ALS244C, SN54AS244A . . . J PACKAGE SN74ALS244C, SN74AS244A . . . DW OR N PACKAGE (TOP VIEW)



SN54ALS244C, SN54AS244A . . . FK PACKAGE (TOP VIEW)



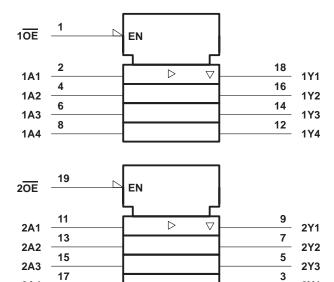
FUNCTION TABLE (each buffer)

INP	JTS	OUTPUT
ŌĒ	Α	Υ
L	Н	Н
L	L	L
Н	Χ	Z

SDAS142C - JULY 1987 - REVISED AUGUST 1995

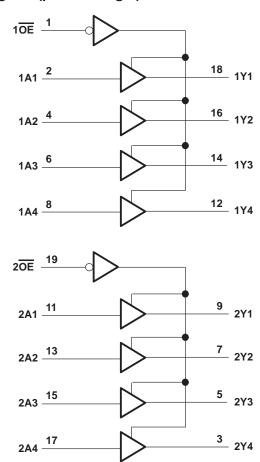
logic symbol[†]

2A4



[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

logic diagram (positive logic)



absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

2Y4

Supply voltage, V _{CC}	7 V
Input voltage, V _I	7 V
Voltage applied to a disabled 3-state output	5.5 V
Operating free-air temperature range, T _A : SN54ALS244C	−55°C to 125°C
SN74ALS244C	0°C to 70°C
Storage temperature range	−65°C to 150°C

[‡] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.



SDAS142C - JULY 1987 - REVISED AUGUST 1995

recommended operating conditions

		SN54ALS244C		4C	SN74ALS244C			UNIT	
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT	
VCC	Supply voltage	4.5	5	5.5	4.5	5	5.5	V	
VIH	High-level input voltage	2			2			V	
\/	Low level input voltage			0.8†			0.8	V	
VIL	Low-level input voltage			0.7‡			MAX 5.5	V	
ІОН	High-level output current			-12			-15	mA	
lo.	Low lovel output ourrest			12				mA	
IOL	Low-level output current						48§] ""A	
TA	Operating free-air temperature	-55		125	0		70	°C	

[†] Applies over temperature range –55°C to 70°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST (CONDITIONS	SN	SN54ALS244C		SN74ALS244C			UNIT
PARAMETER	lE31 C	CNUTTIONS	MIN	TYP¶	MAX	MIN	TYP¶	MAX	UNII
VIK	$V_{CC} = 4.5 \text{ V},$	$I_{I} = -18 \text{ mA}$			-1.5			-1.5	V
	V _{CC} = 4.5 V to 5.5 V	$I_{OH} = -0.4 \text{ mA}$	V _{CC} -2	2		VCC -2	2		
Vari	VCC = 4.5 V to 5.5 V	$I_{OH} = -3 \text{ mA}$	2.4	3.2	3.2 2.4 3.2				V
VOH	V _{CC} = 4.5 V	$I_{OH} = -12 \text{ mA}$	2						v
	vCC = 4.5 v	$I_{OH} = -15 \text{ mA}$				2			
		I _{OL} = 12 mA		0.25	0.4		0.25	0.4	
V_{OL}	$V_{CC} = 4.5 V$	I _{OL} = 24 mA					0.35	0.5	V
		I _{OL} = 48 mA (-1 version)					0.35	0.5	
lozh	$V_{CC} = 5.5 \text{ V},$	$V_0 = 2.7 \text{ V}$			20			20	μΑ
lozL	$V_{CC} = 5.5 V$,	$V_0 = 0.4 V$			-20			-20	μΑ
lj	$V_{CC} = 5.5 V$,	$V_I = 7 V$			0.1			0.1	mA
l _{IH}	$V_{CC} = 5.5 V$,	V _I = 2.7 V			20			20	μΑ
Ι _{ΙL}	V _{CC} = 5.5 V,	V _I = 0.4 V			-0.1			-0.1	mA
IO [#]	V _{CC} = 5.5 V,	V _O = 2.25 V	-20		-112	-30		-112	mA
		Outputs high		9	18		9	17	mA
ICC	V _{CC} = 5.5 V	Outputs low		15	25		15	24	
		Outputs disabled		17	29		17	27	



[‡] Applies over temperature range 70°C to 125°C

[§] Applies only to the -1 version and only if V_{CC} is between 4.75 V and 5.25 V

[¶] All typical values are at V_{CC} = 5 V, T_A = 25°C.

The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, los.

SDAS142C - JULY 1987 - REVISED AUGUST 1995

switching characteristics (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	C _L R1 R2	V_{CC} = 4.5 V to 5.5 V, C_L = 50 pF, R1 = 500 Ω , R2 = 500 Ω , T_A = MIN to MAX †		ν F, Ω, Ω,		
			SN54AL	S244C	SN74AL	S244C		
			MIN	MAX	MIN	MAX		
t _{PLH}	А	V	1	16	2	10	ns	
t _{PHL}		7 Y	3	12	3	10	115	
^t PZH	ŌĒ	V	1	26	3	20	ns	
t _{PZL}	OE	Y	1	24	3	20	115	
^t PHZ	ŌĒ	· ·	2	10	2	10	ns	
t _{PLZ}	OE .	I I	1	26	1	13	115	

T For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

Supply voltage, V _{CC}	7 V
Input voltage, V _I	7 V
Voltage applied to a disabled 3-state output	5.5 V
Operating free-air temperature range, T _A : SN54AS244A	−55°C to 125°C
SN74AS244A	0°C to 70°C
Storage temperature range	-65°C to 150°C

[‡] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

recommended operating conditions

		SN	54AS24	!A	SN	74AS24	1A	UNIT
		MIN	NOM	MAX	MIN	NOM	MAX	UNII
VCC	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
VIH	High-level input voltage	2			2			V
V_{IL}	Low-level input voltage			0.8			0.8	V
ІОН	High-level output current			-12			-15	mA
loL	Low-level output current			48			64	mA
TA	Operating free-air temperature	-55		125	0		70	°C

SDAS142C - JULY 1987 - REVISED AUGUST 1995

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

DADAMETED	TEST 64	NUDITIONS	SN	SN54AS244A			SN74AS244A			
PARAMETER	lesi co	ONDITIONS	MIN TYP† MAX		MIN	TYP†	MAX	UNIT		
VIK	$V_{CC} = 4.5 \text{ V},$	$I_{I} = -18 \text{ mA}$			-1.2			-1.2	V	
	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V},$	$I_{OH} = -2 \text{ mA}$	V _{CC} -2			V _{CC} -2				
\/a		$I_{OH} = -3 \text{ mA}$	2.4	3.4		2.4	3.4		V	
VOH	V _{CC} = 4.5 V	$I_{OH} = -12 \text{ mA}$	2.4						v	
		$I_{OH} = -15 \text{ mA}$				2.4				
\/01	V _{CC} = 4.5 V	$I_{OL} = 48 \text{ mA}$			0.55				V	
VOL	VCC = 4.5 V	$I_{OL} = 64 \text{ mA}$						0.55	v	
lozh	$V_{CC} = 5.5 \text{ V},$	$V_0 = 2.7 \text{ V}$			50			50	μΑ	
lozL	$V_{CC} = 5.5 \text{ V},$	$V_0 = 0.4 V$			-50			-50	μΑ	
IĮ	$V_{CC} = 5.5 \text{ V},$	V _I = 7 V			0.1			0.1	mA	
lн	$V_{CC} = 5.5 \text{ V},$	V _I = 2.7 V			20			20	μΑ	
OE OE	V _{CC} = 5.5 V,	, , , , , , , , , , , , , , , , , , ,			-0.5			-0.5	mA	
IIL A	VCC = 5.5 V,	V _I = 0.4 V			-1			-1	IIIA	
10 [‡]	$V_{CC} = 5.5 \text{ V},$	V _O = 2.25 V	-50		-150	-50		-150	mA	
	V _{CC} = 5.5 V	Outputs high		22	34		22	34		
Icc		Outputs low		60	90		60	90	mA	
		Outputs disabled		34	54		34	54		

switching characteristics (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V C R R T SN54A	UNIT			
			MIN	MAX	SN74A	MAX	
^t PLH	А	.,	2	9	2	6.2	
^t PHL		Υ	1	7	1	6.2	ns
^t PZH	ŌĒ	V	1	10	1	9	ns
t _{PZL}	ÜE	Υ	2	8	2	7.5	115
^t PHZ	ŌĒ	٧	1	6.5	1	6	ns
tPLZ	OL .	ľ	1	10.5	1	9	113

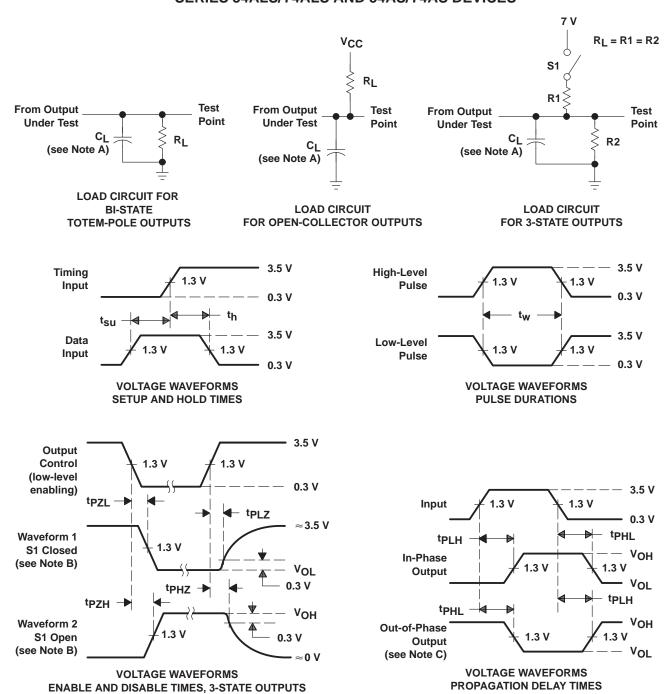
[§] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.



[†] All typical values are at V_{CC} = 5 V, T_A = 25°C. ‡ The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, I_{OS}.

SDAS142C - JULY 1987 - REVISED AUGUST 1995

PARAMETER MEASUREMENT INFORMATION SERIES 54ALS/74ALS AND 54AS/74AS DEVICES



NOTES: A. C_L includes probe and jig capacitance.

- B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- C. When measuring propagation delay items of 3-state outputs, switch S1 is open.
- D. All input pulses have the following characteristics: PRR \leq 1 MHz, $t_f = t_f = 2$ ns, duty cycle = 50%.
- E. The outputs are measured one at a time with one transition per measurement.

Figure 1. Load Circuits and Voltage Waveforms



IMPORTANT NOTICE

Texas Instruments and its subsidiaries (TI) reserve the right to make changes to their products or to discontinue any product or service without notice, and advise customers to obtain the latest version of relevant information to verify, before placing orders, that information being relied on is current and complete. All products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgement, including those pertaining to warranty, patent infringement, and limitation of liability.

TI warrants performance of its semiconductor products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

CERTAIN APPLICATIONS USING SEMICONDUCTOR PRODUCTS MAY INVOLVE POTENTIAL RISKS OF DEATH, PERSONAL INJURY, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE ("CRITICAL APPLICATIONS"). TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS. INCLUSION OF TI PRODUCTS IN SUCH APPLICATIONS IS UNDERSTOOD TO BE FULLY AT THE CUSTOMER'S RISK.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards must be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance or customer product design. TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used. TI's publication of information regarding any third party's products or services does not constitute TI's approval, warranty or endorsement thereof.

Copyright © 1998, Texas Instruments Incorporated